

Curriculum Vitae – Tsvi G. Dvorkind

Personal details

Marital Status: Married+3

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Education

2004-2007: **Ph.D.** in Electrical Engineering, Technion.

2001-2003: **M.Sc.** in Electrical Engineering, Technion. (summa cum laude)

1997-2000: **B.Sc.** in Electrical Engineering, Technion. (summa cum laude)

Experience

2007-Present: Senior researcher, GNSS group, RAFAEL, Israel.

2002-2007: Consulting services in the field of signal processing. Teaching assistant, projects supervisor, signal and image processing lab (SIPL), EE, Technion.

2000-2001: Jigami corporation. Software engineer at the R&D group.

1998-2000: EORD (electro-optics research & development company, Ltd).
Software development of signal processing applications.

Awards and honors

2011: Research Fellow, RAFAEL.

2009: Katzir Fellowship, MOD.

2003: Gutwirth scholarship.

1998,2000: President Excellence Award, Technion.

1999: Provost Excellence Award, Technion.

1997,1998,1999: Dean Excellence Award, Technion.

Patents

'METHOD AND APPARATUS FOR RECONSTRUCTING DIGITIZED DISTORTED SIGNALS' - , US

patent office. Patent No. 8,228,218