

Ilan Feldman

☎ 053-2766-566 ✉ ilanff@gmail.com

A software engineer with 4 years of experience, looking for a software developer position.

Also possess a strong hardware background with 7 years of experience.

Highly motivated, a team player, very adaptive, and self-taught.

Eager to learn new technologies, concepts and areas. Would love to make an impact through code and collaboration.

Experience:

2017-Present **Senior Software Engineer, Synopsis**

- Design, implementation and maintenance in projects for world-leading HW verification tools, including:
 - Non-relational, proprietary, performance-oriented DB capturing the largest chip designs (such as Intel processors)
 - Infrastructure and key part of a new **compilation time optimization**
 - Client execution time optimization by introducing **multithreading**
 - Centralizing **client access management** for improved scalability and performance
 - New compiler capability and flow for making System Verilog dynamic array synthesizable, thus possible to emulate. This **enabled customers** to improve their verification testing
 - Testing infrastructure for **improved coverage** of the multithreaded optimization project
 - Enhancing an emulation flow to support advanced Verilog constructs like cross module references and System Verilog assertion control commands
- Collaborating locally and across countries and time zones - project team, clients, QA Engineers
- Boosting productivity- writing scripts, adopting tools and formulating work methodologies

2016-2017 **RT Embedded Software Engineer, Microsoft**

- Design and coding of Firmware (uC) code in C++
- Self-initiated Python and windows (batch) scripting for more efficient unit testing and debug
- Design and coding of I2C host platform in Python
- Automating firmware emulation/test platform in C++

2015-2016 **FPGA Engineer, Orsan-Medical**

- Ownership of all FPGA logic (Verilog, SV), including physiological signals processing
- Leadership and execution of HW-FPGA-SW interfaces end-to-end implementation and integration
- Self-Initiated modernization and improvement of source control by assimilating Git

2008-2014 **Israeli Air Force**

2012-2014 **R&D and Integration Engineer**

- HW design leadership, key figure in design and research for new products (POC)
- Reverse engineering legacy systems
- Connectivity and integration testing in UNIX environment

2011-2012 **Project Manager Officer**

- Technical lead in system Engineering and communication channel engineering (spec & design)
- Managing deployment and assimilation of the ground section of a communication network

2008-2011 **Hardware Engineer**

- Specification, design, implementation and verification of new logic (VHDL) and integration of commercial/military
- Design and implementation of Analog-Digital and FPGA-SW interfaces

Education:

2009-2011 **Advanced EE courses, including stochastic processes and detection theory – Tel Aviv University**

2004-2008 **B.Sc, Electrical and Electronics Engineering – Tel Aviv University, Cum Laude**

Practical and Professional skills:

Programming Languages: C++14/11, Python, C, Assembly

Source Control & IDEs: Git, Perforce, Tfs, Visual studio, QtCreator, Cscope+vim

OS & Other languages: Linux, Windows, Verilog, VHDL, System-Verilog, Matlab

Miscellaneous: OOP, Design Patterns, STL, GDB, Boost, csh, bash, HW Lab equipment, Jira

Languages:

English- Fluent Hebrew- Native Russian-Native/Fluent