

Gilinsky Arkady

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Profile:

A senior software engineer with a strong technical skills and experience in a different HW and SW aspects of computer field. Ability to work as an individual or as a part of a team. Have fast self learning and out-of-box thinking skills.

Good understanding of computer HW & SW parts organization and functioning, network protocols, TCP/IP stack and it's Linux implementation details, compiler and linker process,

Linux user space area and Linux kernel functioning. Rich experience with working in cross-platform environment and board bring-up process. Deep knowledge of HW low level protocols: I2C, MDC/MDIO, SPI, PCI etc. Strong programming skills in python,C, shell scripting and some other languages. Have a good SW security background. Understand principles of kubernetes architecture. Linux and Open Software fan and geek.

Work experience:

Sep. 2017 - : "Harmonic Inc" company. Qeysaria, Israel.

Position: Virtualization team, Senior SW engineer.

- Virtual Cable Management Termination System
- Working with Kubernetes container orchestration systems
 - * Platform preparing
 - * Kunernetes configuration
 - * Different plugin integration
 - * Involved in open source plugin development (GitHub)
 - * Docker images building and interactions
 - * Analyzing and improving security aspect of the cluster system in k8s environment

Jul. 2014 - Sep. 2017: "Harmonic Inc" company. Qeysaria, Israel.

Position: Low level team, Senior SW engineer.

- Board bring-up
- Low level system blocks integration. (like systemd services)
- Firmware design & debugging
- Linux kernel integration
- U-Boot features implementation & integration
 - * SW tools: Python, C, Bash scripting
 - * HW tools: JTAG debugger, oscilloscope
 - * Environment: Linux (Debian Jessie distribution)
 - * HW platform & devices: ARM based SoC

Oct. 2012 - Jul. 2014: "Intel" company. Haifa, Israel.

Position: SW security engineer.

- Code analyzing for security vulnerabilities.
- Penetration testing.
 - * SW tools : Microsoft Visual Studio, AIDA, Python, Perl, shell scripting

* HW tools: JTAG debugger
Environment : Windows NT, Linux, UEFI
HW platform & devices: X86

May 2005 - Sept. 2012: "EZchip Technologies Ltd." company (10Gb network processors). Yoqneam, Israel.

Position: Senior software engineer target board support team.

- Take part at the NP TBS package development (NP-3, NP-4, NPA,NPA0)
- Linux kernel 2.6 porting to the specific target board (MIPS SMP(2 cores) based SOC)
- Different Linux kernel drivers for board peripheral devices for SOC components
- Linux root file system design and integration (MIPS based: Crosstool-ng + Buildroot)
- Porting and integration u-boot to the specific target MIPS based SOC board
- Design, implementation and support cross-platform compilation environment
- Linux kernel 2.6 porting to the specific target board (PPC85xx based)
- Linux kernel debugging via HW debugger
- Porting and integration u-boot to the specific target board (PPC85xx based)
- Flash based file system design and creation for target board (PPC85xx based: Crosstool-ng + Buildroot)
- Linux kernel (2.4 and 2.6) module design and implementation for target board support.
- Design and implementation of board support application (User-space).
- Design and implementation of board diagnostic application for Linux OS (user-space application + kernel module support).
- Multi-platform testing environment for target board support module.
- HW debug interactive application for board bring up and debugging (Linux/VxWorks).
- Board bring up and HW/SW problems debugging on Linux/VxWorks.
- Support Target Board Software package for different boards and platform.

SW tools : Microsoft Visual C++, Tornado 2.0, Perl, assembler (MIPS, PPC, x86), C, vi, shells, git (Version control system), Crosstool-ng, Buildroot

HW tools: JTAG debugger, Logic analyzer, oscilloscope

Environment : Windows NT, VxWorks, Linux, u-boot

HW platform & devices: X86, PowerPC, MIPS, different PHY's, MAC's, microcontrollers & sensors

Apr 2000 – May 2005 : "EZchip Technologies Ltd." company (10Gb network processors). Yoqneam, Israel.

Position: Software engineer NP driver team

- Take part at the NP driver development (NP-1, NP-1c, NP-2,NP-3) module for complex search structure creating and managing algorithm optimizations driver integration and bring up
- CAM middle layer SW implementation (Cypress, Netlogic, IDT)
- Implementation of automatic testing environment (perl-based)
- Driver checking and verification
- Porting driver to Linux

Tools : Microsoft Visual C++, Tornado 2.0, Perl, assembler, C , C++

Environment : Windows NT, VxWorks, Linux

Education:

1990 - 1995 MS in System Engineer.

Yaroslavl State University, Computer system and networks faculty. Russia.

Training courses:

- Linux kernel internals & Device drivers
- TCP/IP stack
- VxWorks RT programming
- Linux kernel programming
- EZchip NP microcode programming
- And many more ...

Languages:

- Russian (native)
- English
- Hebrew